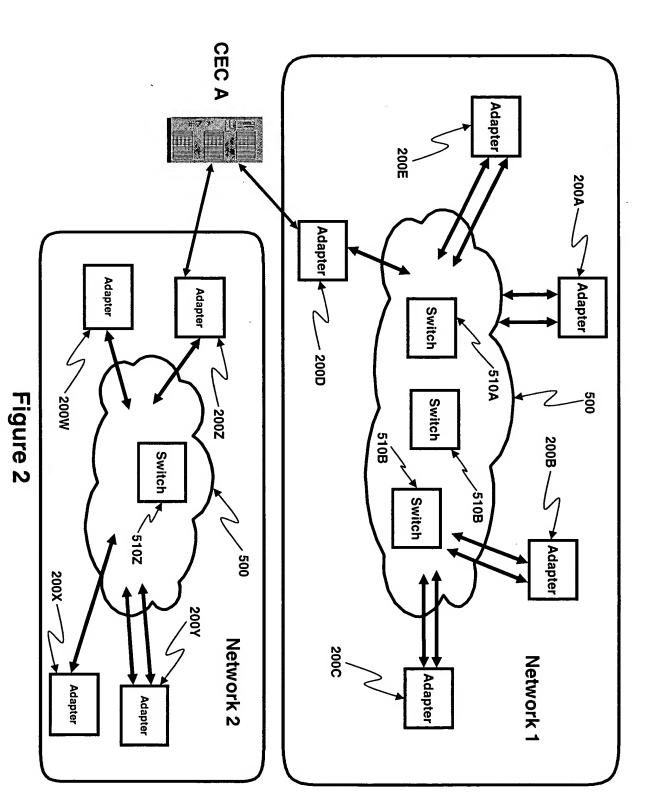


€.; }.



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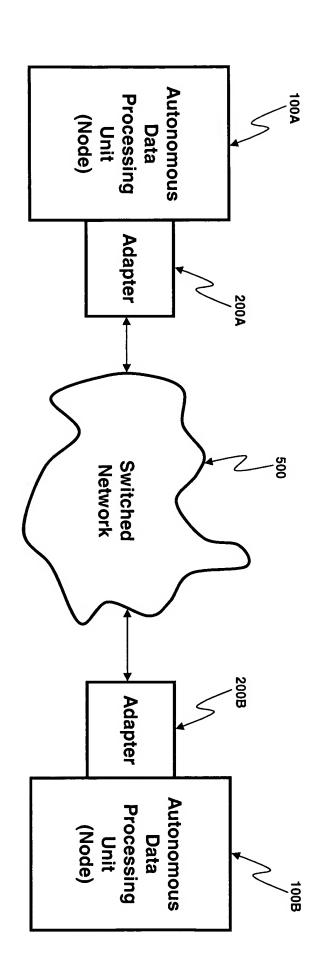


Figure 3

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Send-Receive



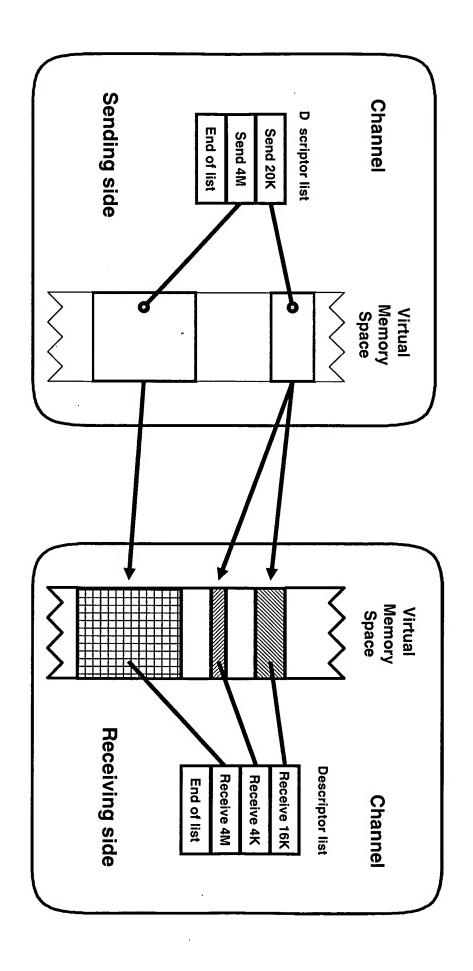


Figure 4

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Remote Read-Write

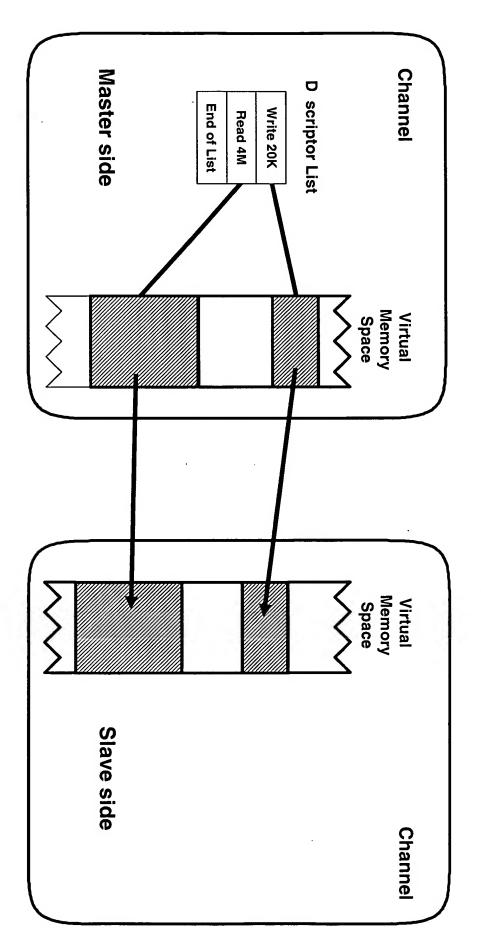


Figure 5

£\

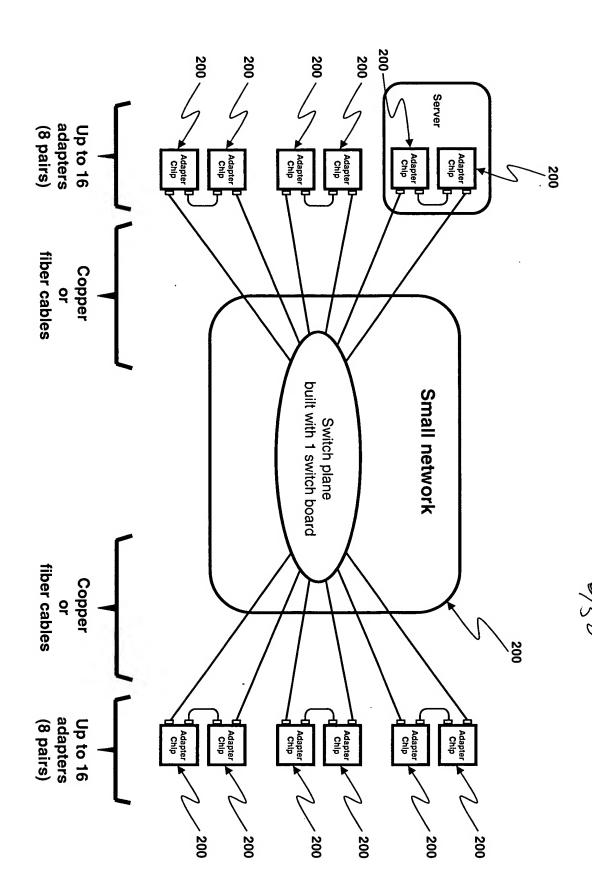


Figure 6

۵

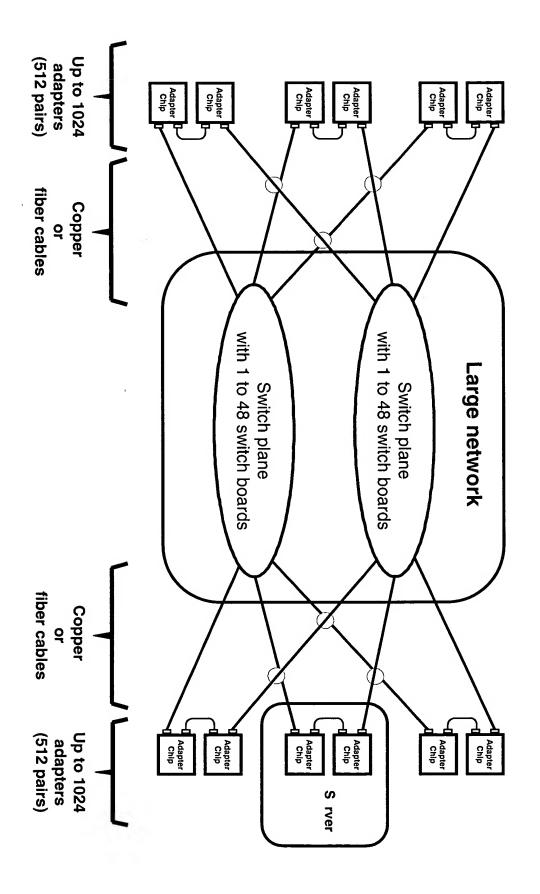


Figure 7

9/50

S

Programming Interface

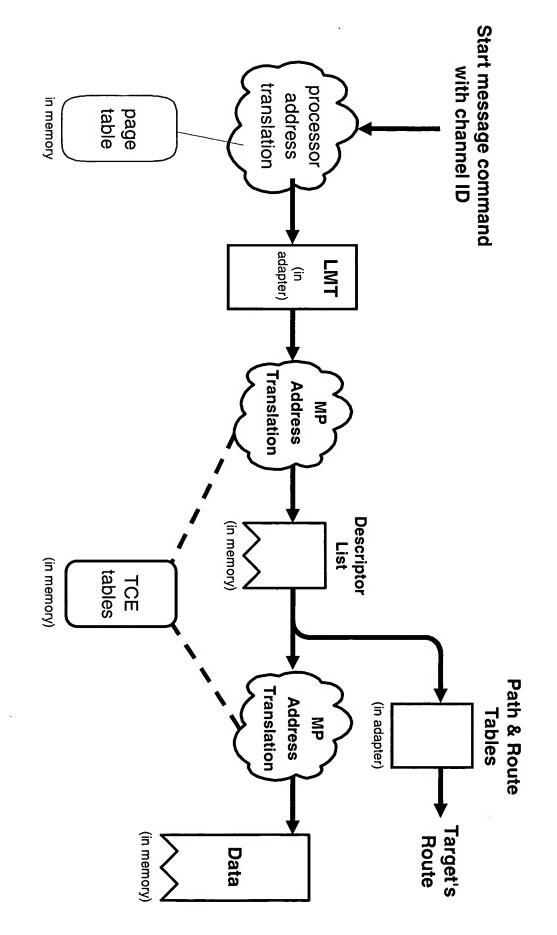


Figure 8

€3

Address Translation



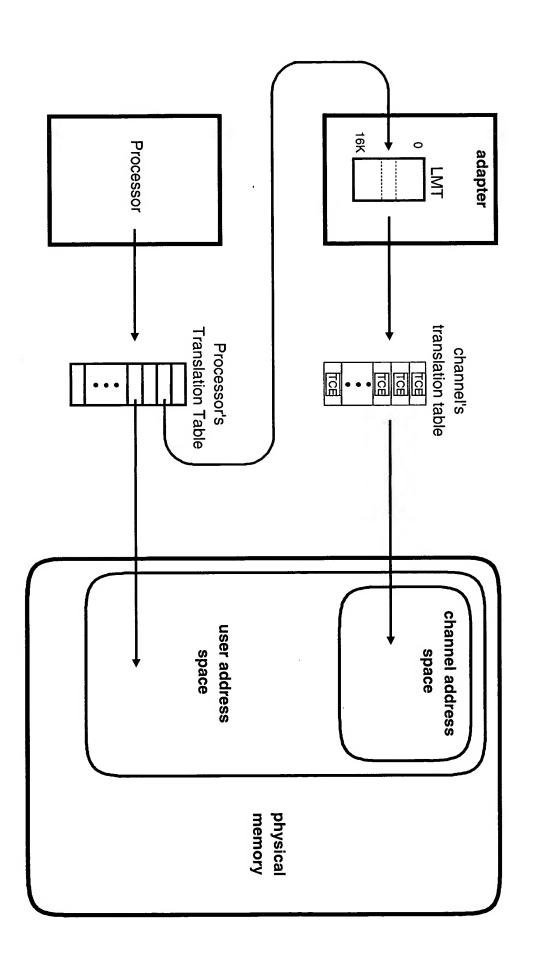


Figure 9

Address Translation with 4K pages

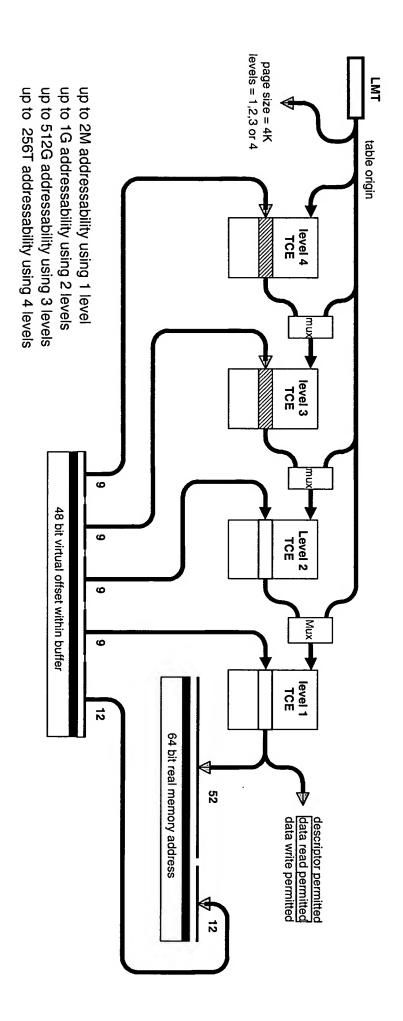


Figure 10

0



Address Translation with 16M pages

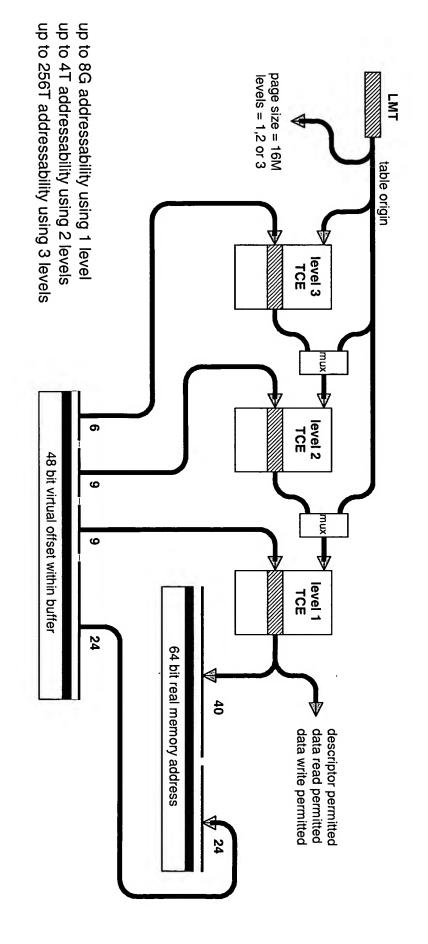


Figure 11

Adapter Identification

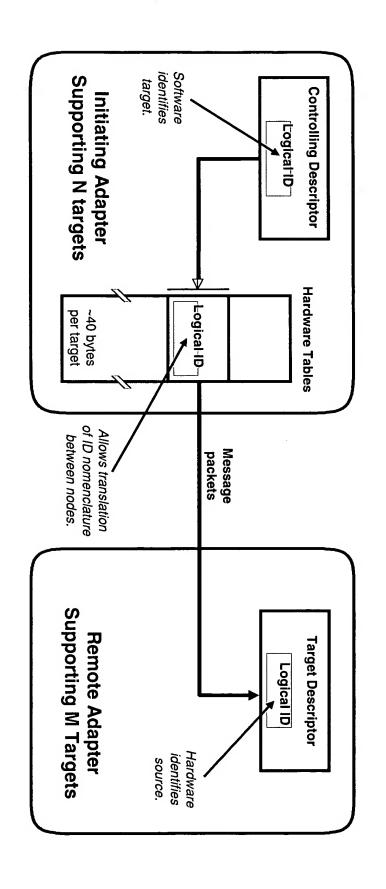


Figure 12

Broadcast Function

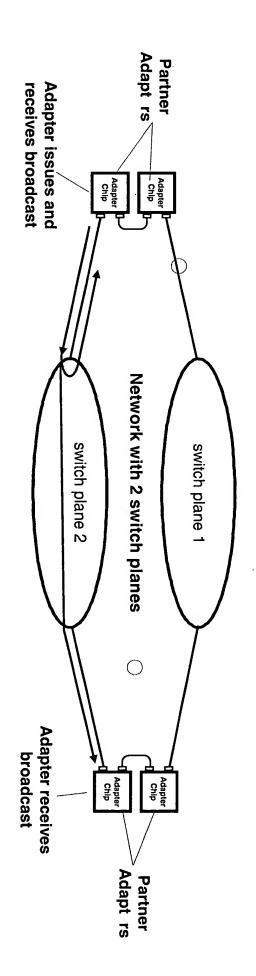


Figure 13

Processor Interrupts

Channel Interrupt Generation

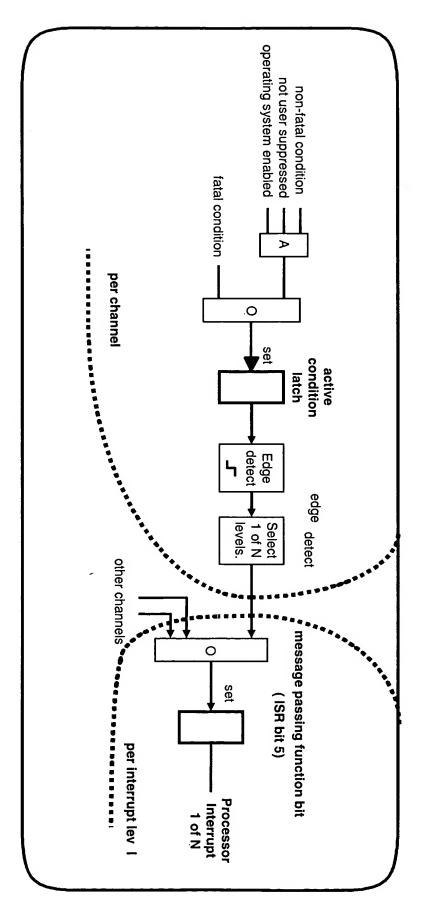


Figure 14

Defined Address Fields

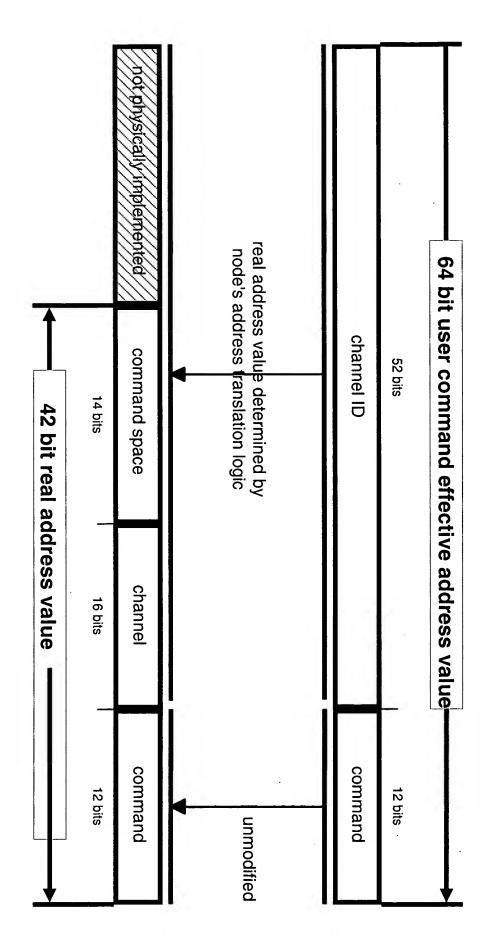


Figure 15

Channel States

Resume

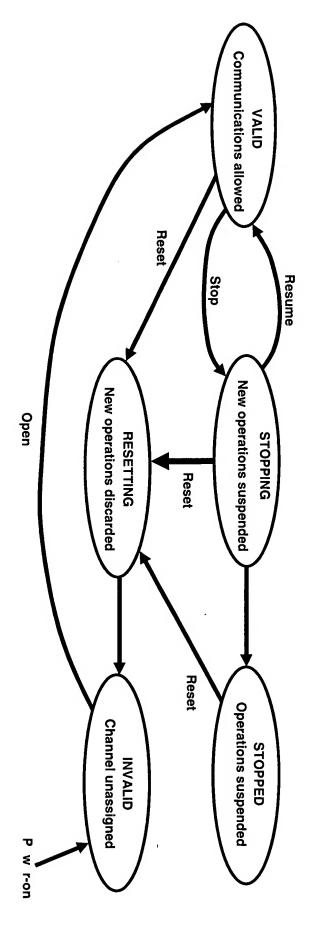


Figure 16

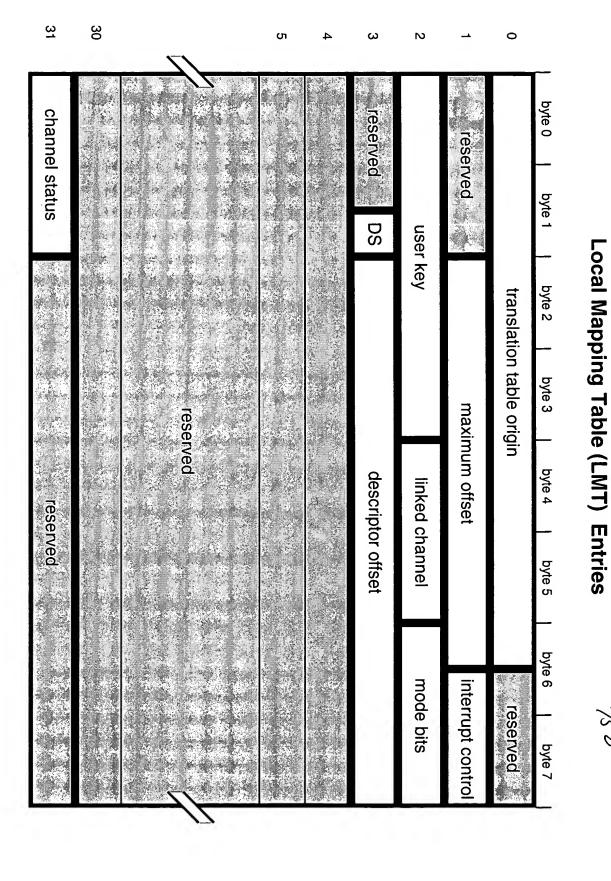


Figure 17

Translation Control Elements

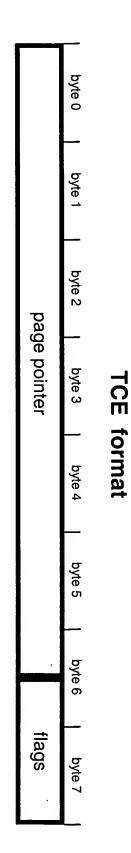


Figure 18

Remote Write

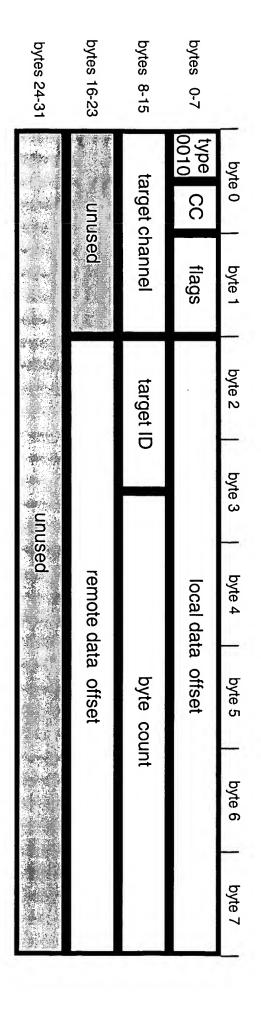


Figure 19

Remote Read

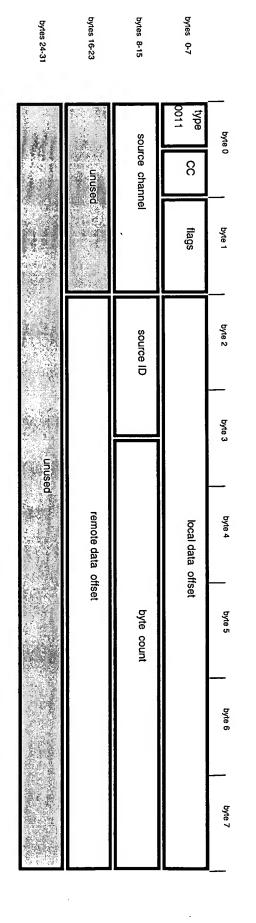


Figure 20

Source of Push

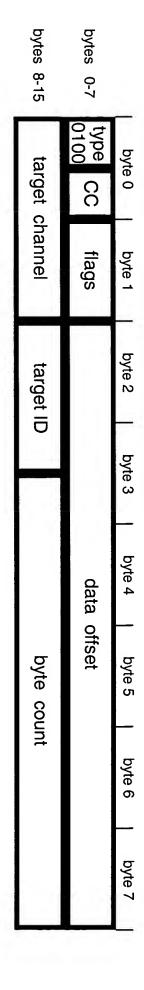


Figure 21

Target of push

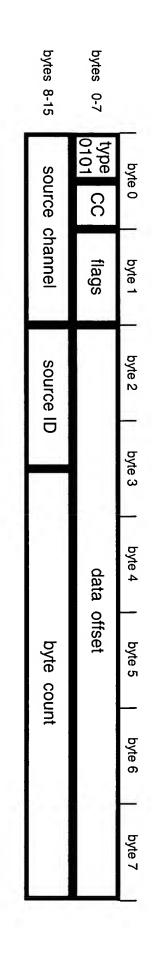
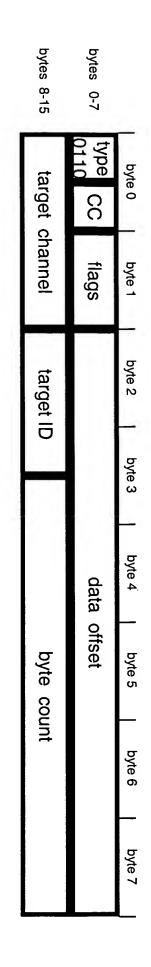


Figure 22

Source of Pull



Target of pull

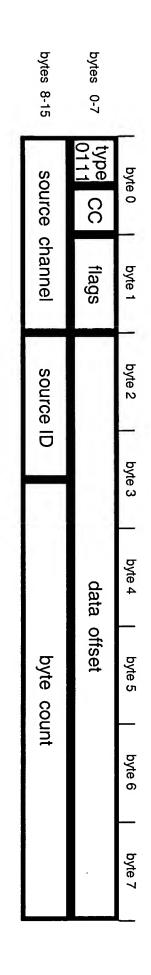


Figure 24

Preload data

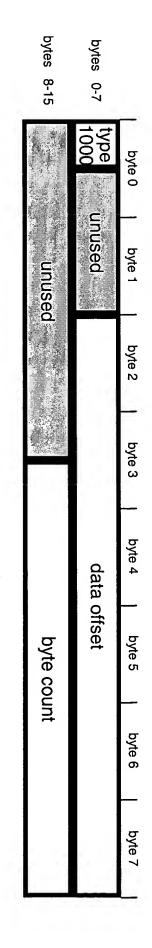


Figure 25

Branch

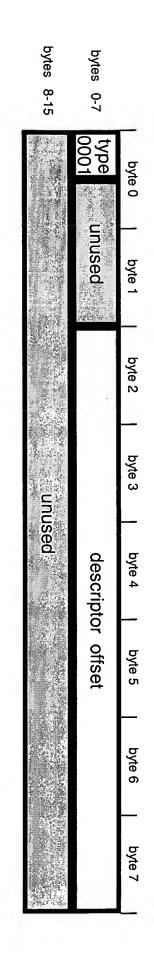


Figure 26

Path Table Entry

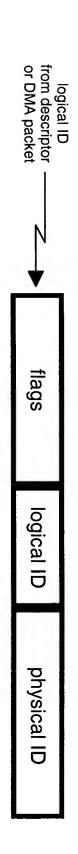


Figure 27



Route Table Entry

Figure 28

Broadcast Registers

	byte 0 byte 1	byte 2	<i>N</i>
egister 0	lookup table index	port	
egister 1	lookup table index	port	
egister 2	lookup table index	port	
egister 3	lookup table index	port	

Figure 29

Sequence Table Entry

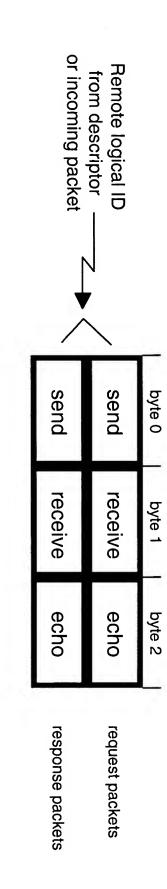


Figure 30

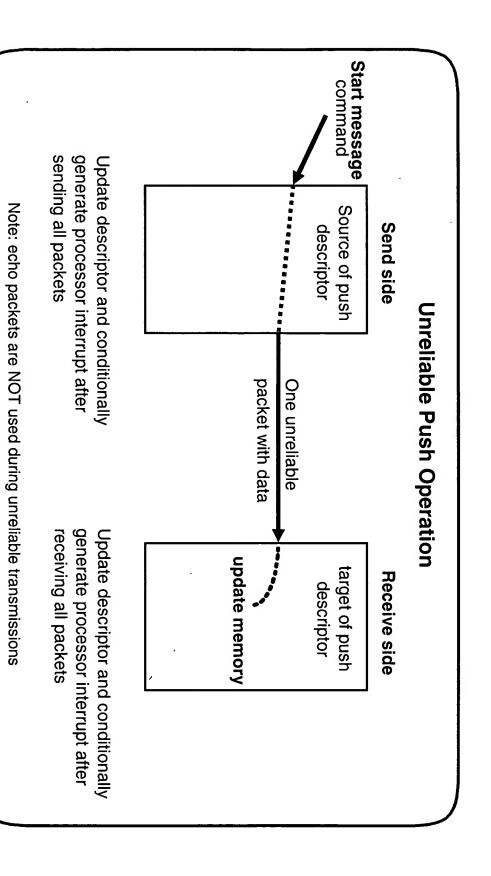


Figure 31

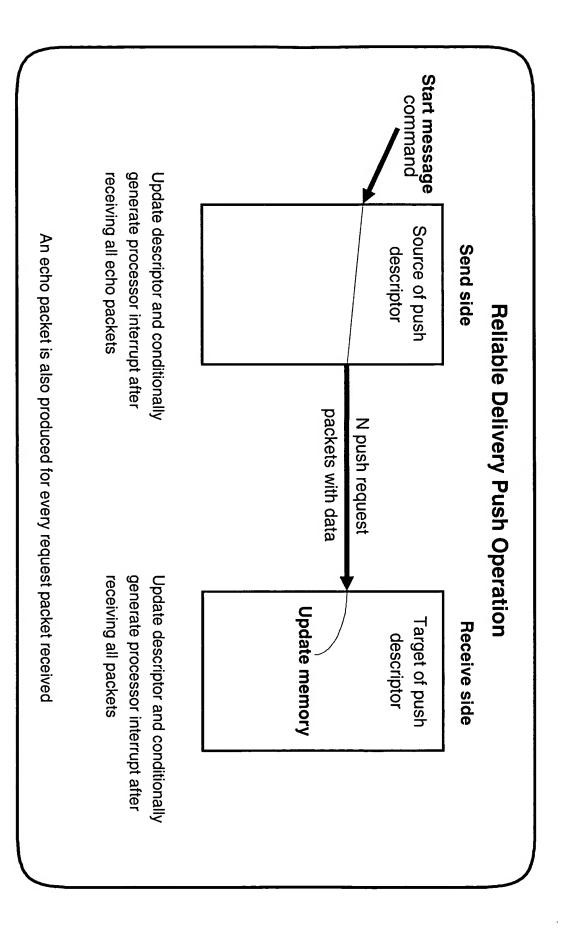


Figure 32

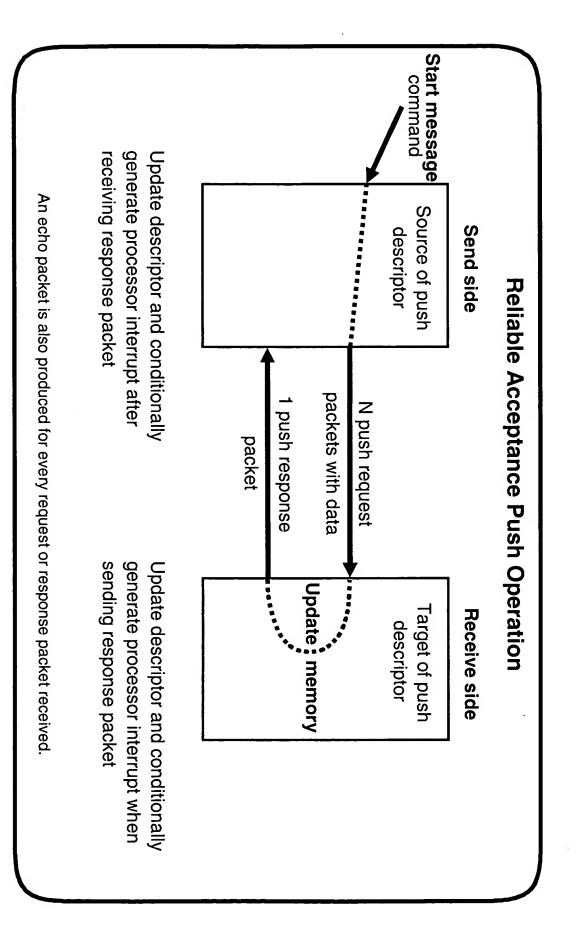


Figure 33

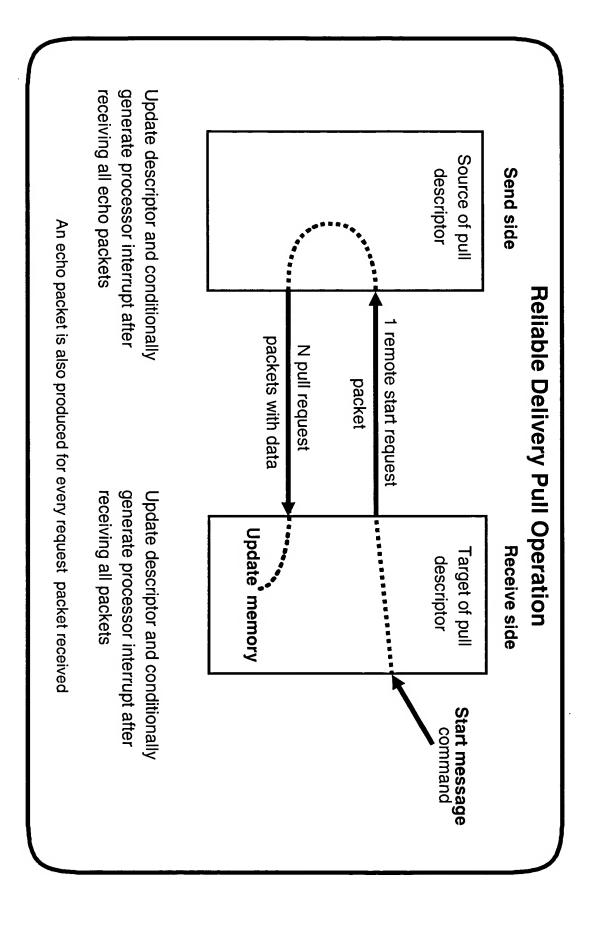


Figure 34

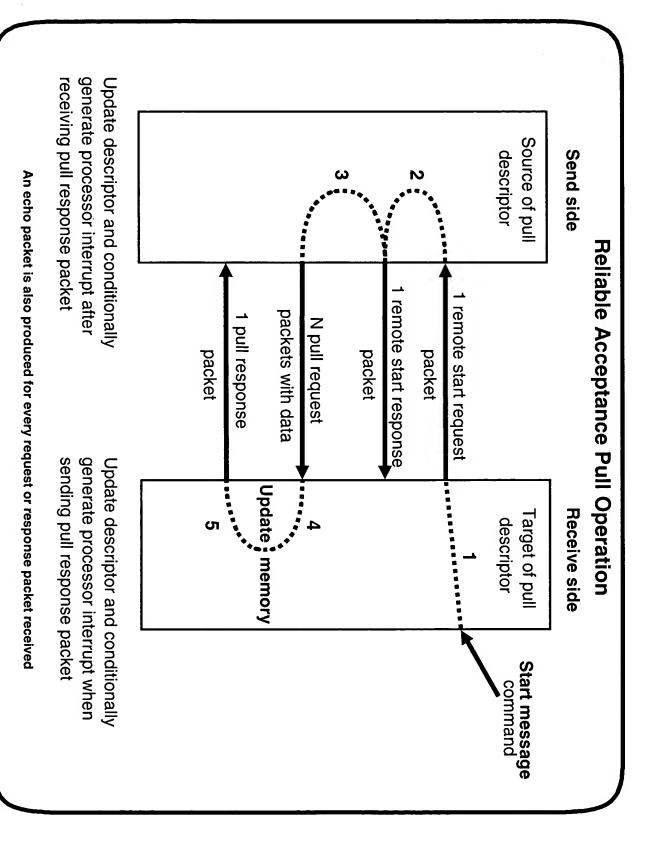


Figure 35

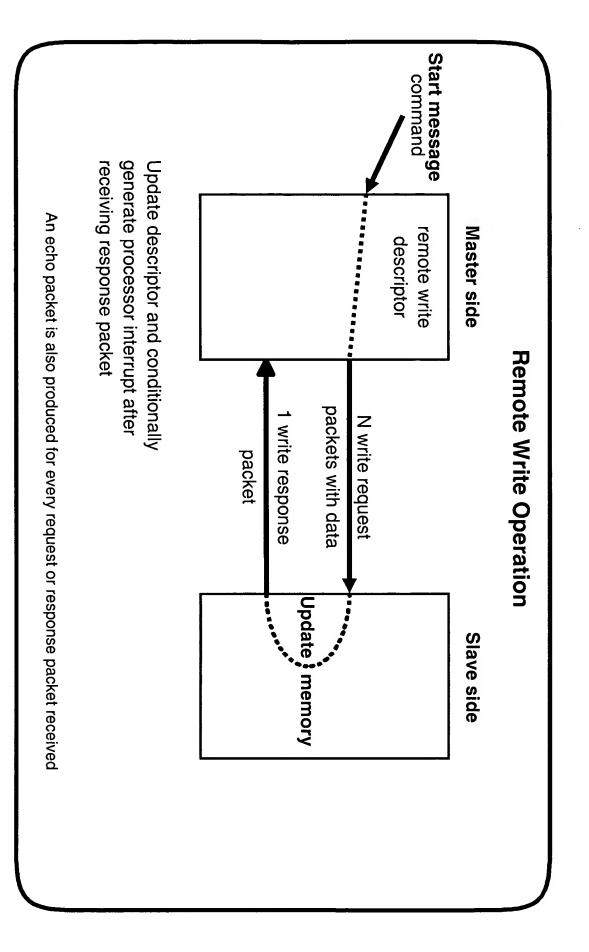


Figure 36

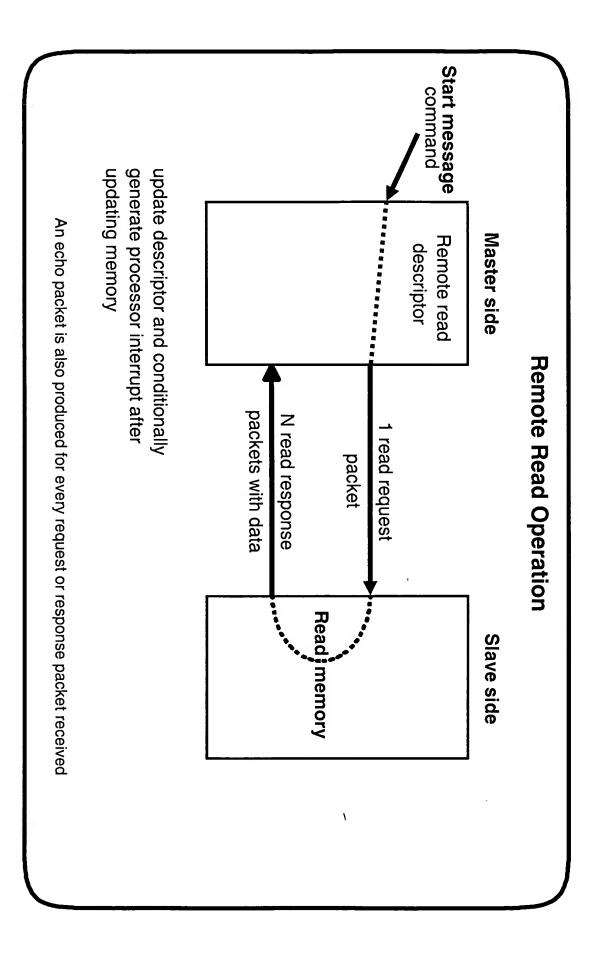
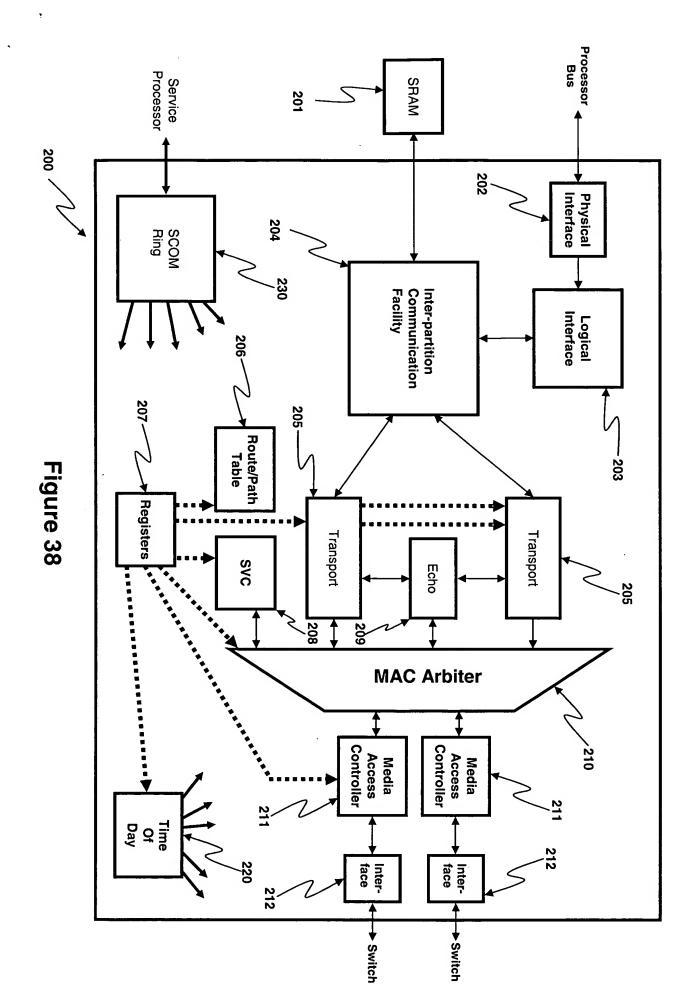


Figure 37



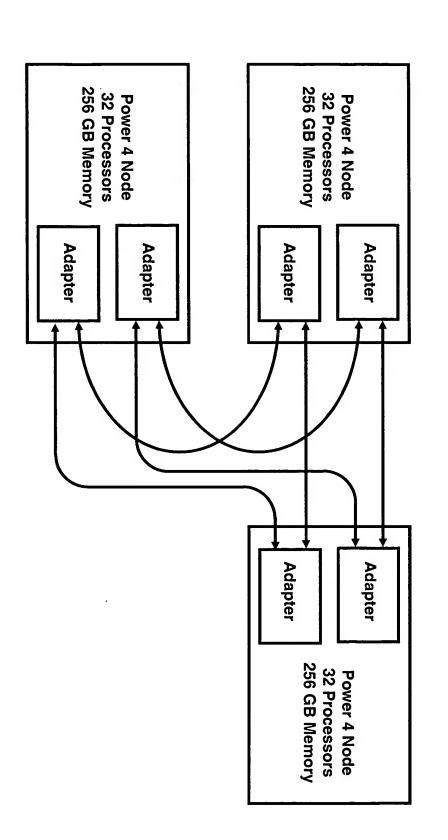
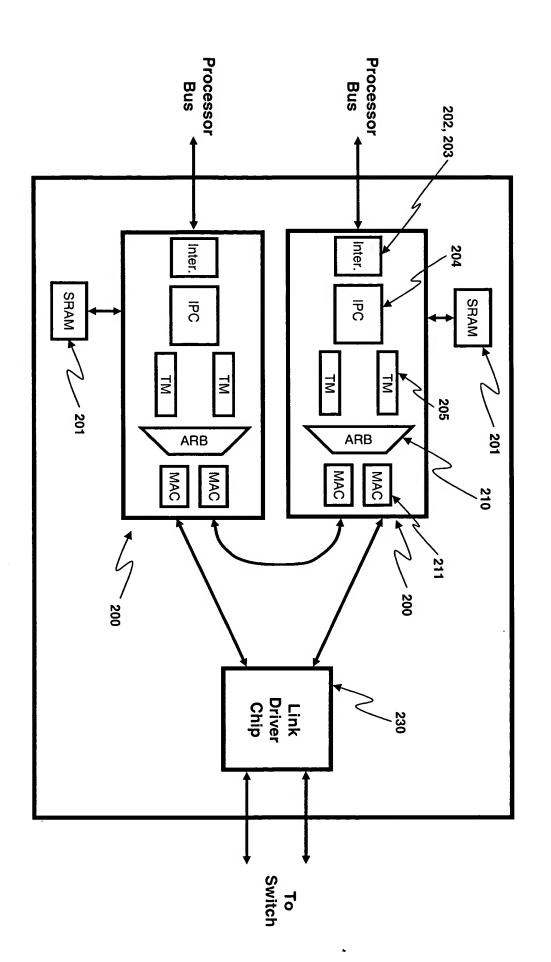


Figure 39



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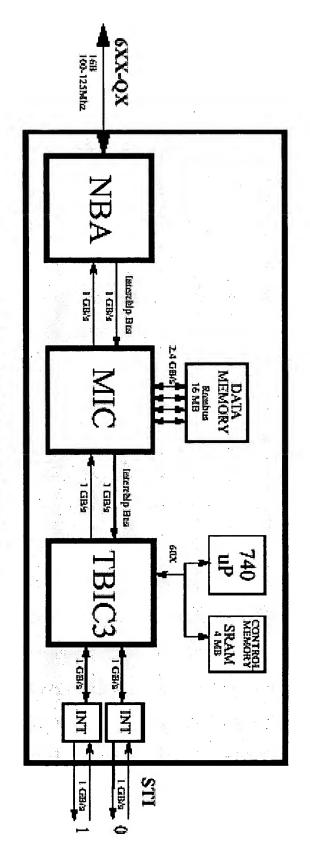
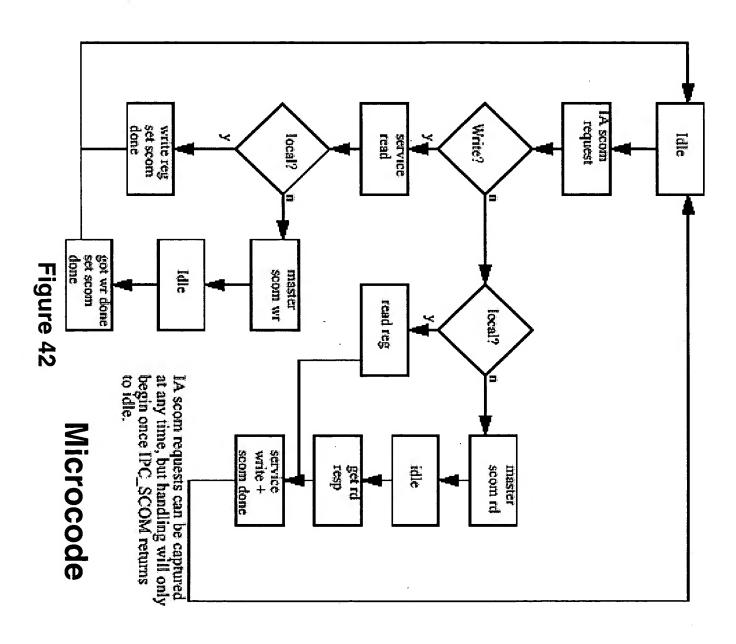


FIGURE 10. Colony Adapter

Figure 41

From page 32 of the spec - inclusion speculative



AUTO TRACKING OF BAD PATHS

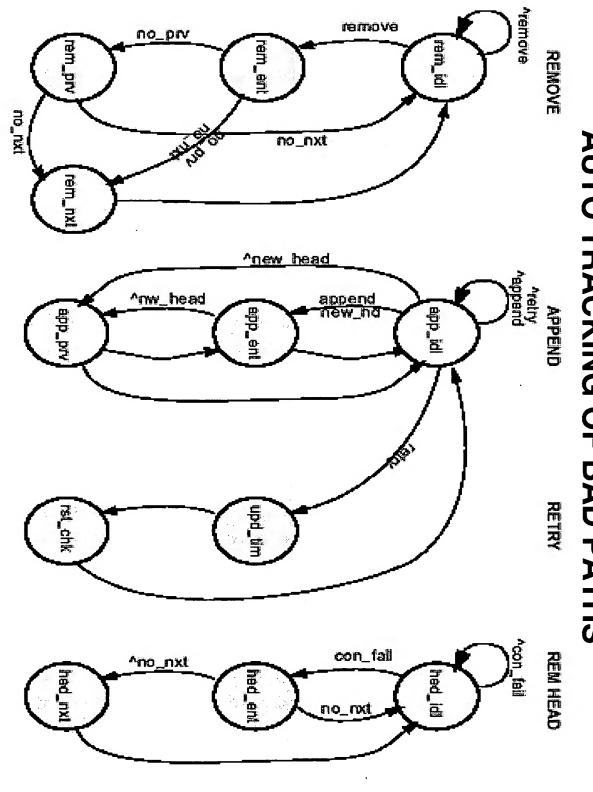


Figure 43



Formatter

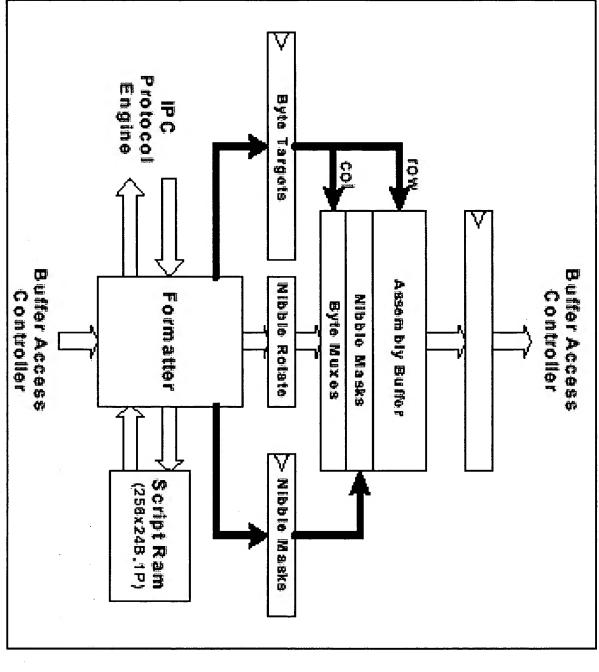
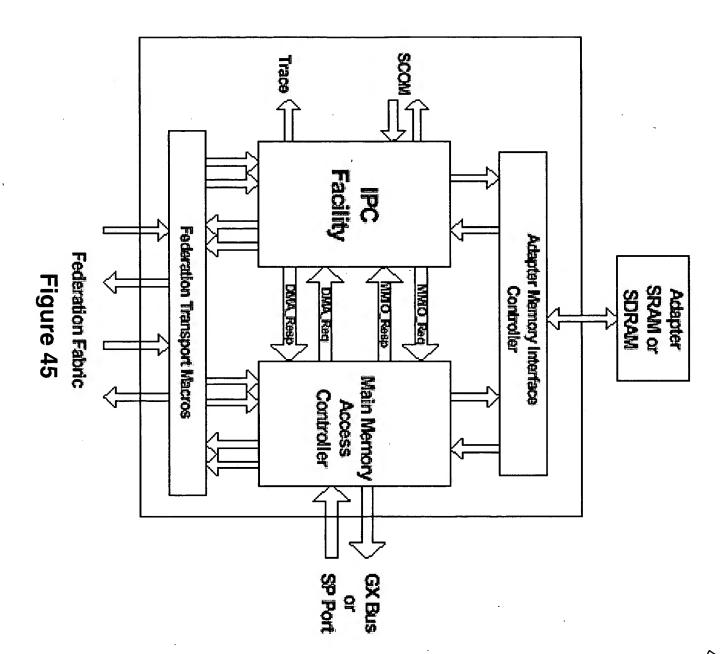


Figure 44

5/4 to





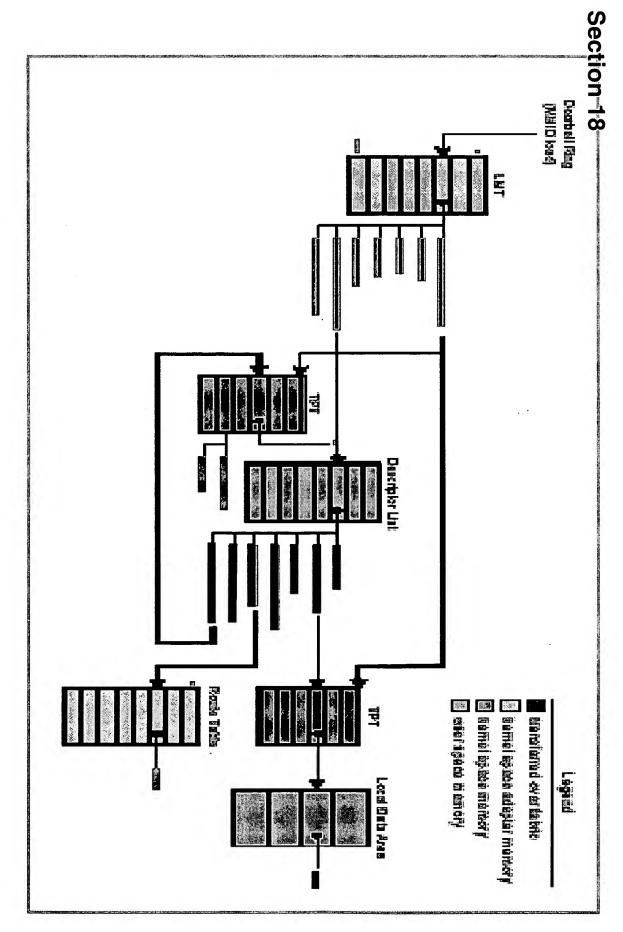


Figure 46

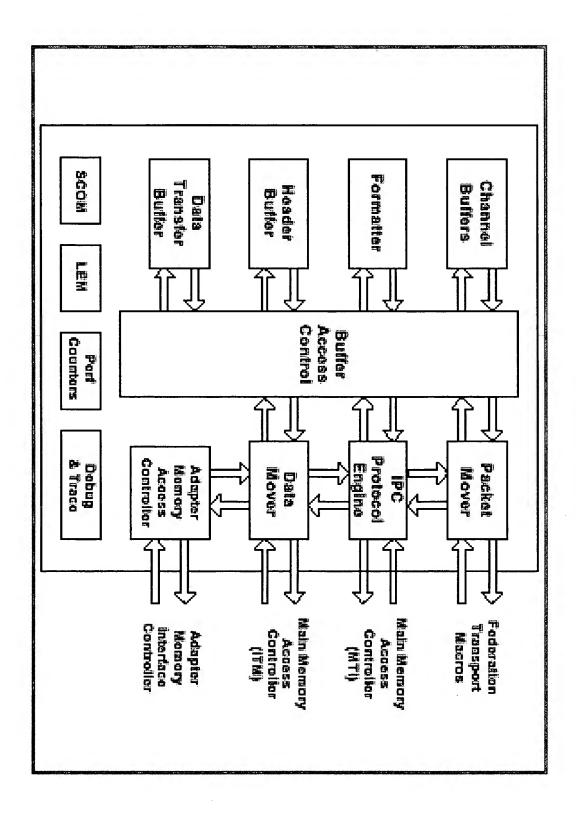
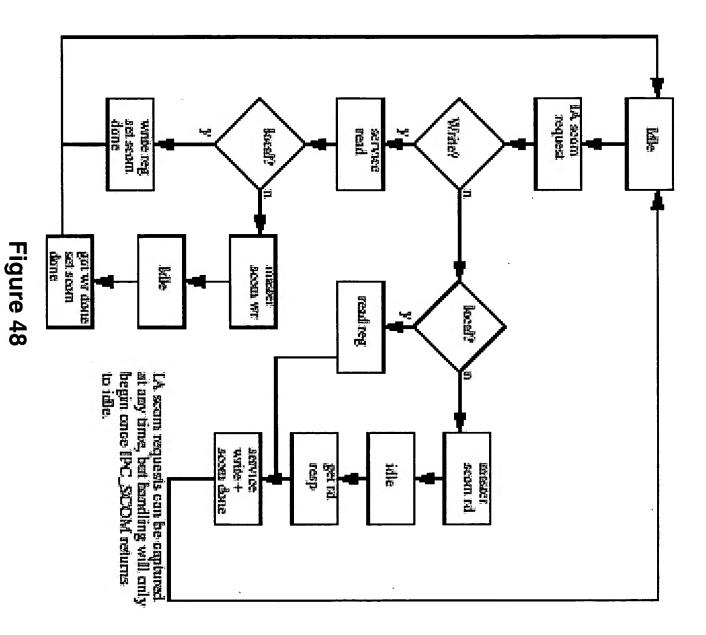
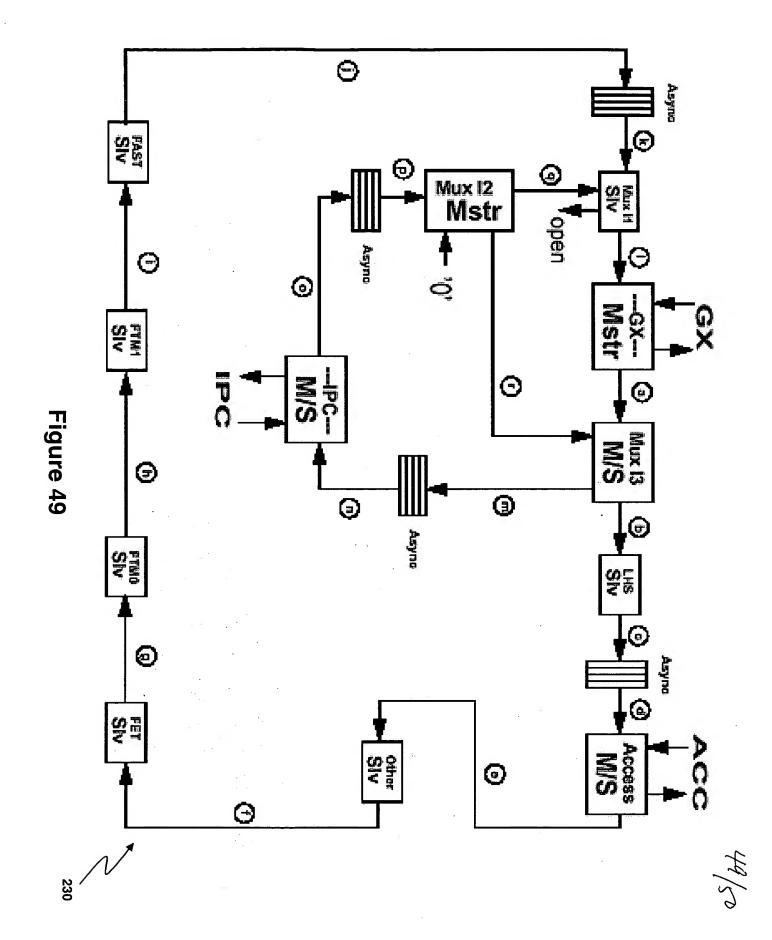


Figure 47

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IPC Protocol Engine

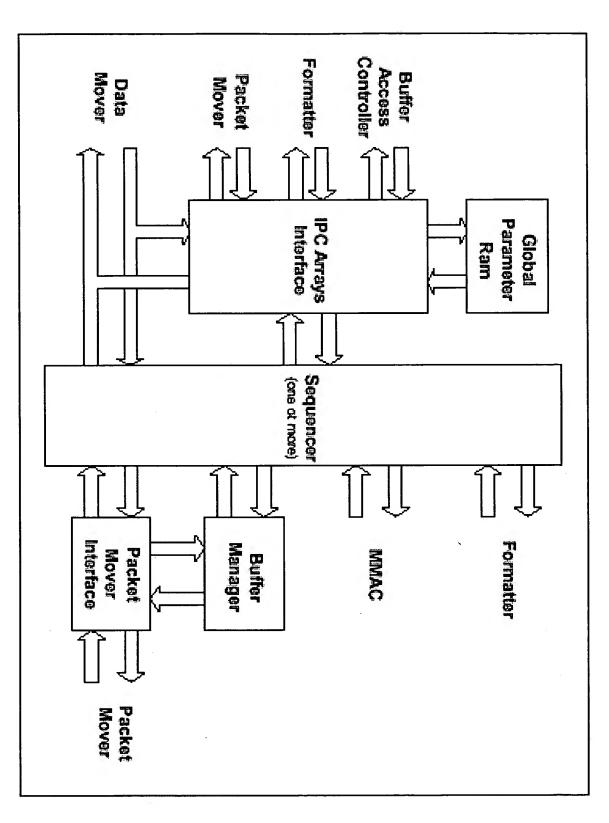


Figure 50